

Docket No. AT9-98-194

Figure 6 is a diagram depicting the saving of data from VRAM to a buffer in accordance with a preferred embodiment of the present invention;

5 **Figure 7** is a flowchart of a process for restoring the contents of a video graphics adapter in accordance with a preferred embodiment of the present invention;

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Figures 8A-8C are illustrations of pseudo code 8D employed in a device driver when an application switches to the foreground;

10 **Figures 9A-9C** are illustrations of pseudo code 9D employed in a device driver when an application switches to the background;

15 **Figure 10** is a high level flowchart of a process for capturing I/O ports in accordance with a preferred embodiment of the present invention;

Figure 11 is shown below in **Figures 11A-11B**, which are diagrams of pseudo code used to capture port addresses in accordance with a preferred embodiment of the present invention; and

20 **Figure 12A-12C** are diagrams of pseudo code used to set modes in accordance with a preferred embodiment of the present invention.